Abstracts

A High Performance 2.4GHz Tranceiver Chip-Set for High Volume Commercial Applications

B. Khabbaz, A. Douglas, J. DeAngelis, L. Hongsmatip, V. Pelliccia, W. Fahey and G. Dawe. "A High Performance 2.4GHz Tranceiver Chip-Set for High Volume Commercial Applications." 1994 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 94.1 (1994 [MCS]): 10-14.

A high performance MMIC transceiver chip-set has been developed for the 2.4GHz to 2.5GHz ISM band. The chip set consumes low power, making it ideal for battery powered FSK systems such as portable powered computer WLAN and portable data collection terminals. The die sizes are also very small, making them cost effective for high volume commercial applications. All transceiver functionality, including a VCO, are integrated onto a single chip, 1.4mm x 2.79mm die size. This chip has a very low power dissipation of 175mW on receive and 225mW on transmit. The second chip contains T\R and diversity switches, a 10dB step attenuator, and the output power amplifier.

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